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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/281,164	03/30/1999	KATSUHIKO NAGATA	1232-4527 6886	
7590 10/05/2004			EXAMINER	
MORGAN & FINNEGAN L.L.P. 345 PARK AVENUE			TRAN, NHAN T	
NEW YORK, NY 10154			ART UNIT	PAPER NUMBER
			2615	

DATE MAILED: 10/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/281,164	NAGATA ET AL.				
Office Action Summary	Examiner	Art Unit				
	Nhan T. Tran	2615				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply 1 ft NO period for reply is specified above, the maximum statutory period was pailing to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tin within the statutory minimum of thirty (30) day rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed vs will be considered timely. I the mailing date of this communication. ED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 22 Ju	ily 2004 and 27 May 2004.					
2a) This action is FINAL . 2b) ⊠ This						
3) Since this application is in condition for allowar	•					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposition of Claims						
4) ☐ Claim(s) 24-28 is/are pending in the application 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 24-28 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration.					
Application Papers						
9) The specification is objected to by the Examiner.						
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the		, ,				
Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Ex		- , ,				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Applicati ity documents have been receive (PCT Rule 17.2(a)).	ion No ed in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail Do 5) Notice of Informal F 6) Other:	ate Patent Application (PTO-152)				

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 7/22/2004 and 5/27/2004 has been entered.

Response to Arguments

2. Applicant's arguments with respect to claims 24-28 have been considered but are moot in view of the new ground of rejection.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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3. Claims 24-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Onuma Takashi (JP 02-131226) in view of Kohno et al (US 5,029,991) and in further view of Walsh et al (US 5,848,253).

Regarding claim 25, Onuma discloses a lens device comprising:

a camera interface component (2, 3) that conforms a lens control signal to a predetermined data format (predetermined voltage and polarity for controlling a stop) of the lens device, wherein the lens device is attachable to plural cameras employing different data formats (voltages and polarities) for lens control signal (for controlling aperture stops), wherein the data formats differ by at least control voltage and polarity, and the lens control signal is received from the attached cameras (see Fig. 2 and Abstract).

Onuma does not explicitly show two different circuit boards that are employed for mounting plurality of components, i.e., 2-6 as shown in Fig. 2, for interfacing and controlling the lens device. Kohno teaches that since electrical components of varied kinds including among others electrical elements having large chip areas, such as a CPU, etc. are necessarily arranged in an interchangeable lens device, the use of a single printed circuit board hardly provides sufficient large space for mounting them. The use of two or more printed circuit boards has become necessary. Thus, two printed circuit boards (80 and 90) are employed for mounting camera interface components and other driving components to communicate with a camera and control the lens device. The two printed circuit boards are connected together through a communication line (82). See Kohno, col. 3, lines 33-61 and col. 4, line 45.

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Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention was made to implement in the lens device of Onuma a camera interface board that conforms a lens control signal to a predetermined data format of the lens device and a lens circuit board to control lens device, wherein the lens circuit board is connected with the camera interface board through a communication line, in an obvious arrangement of electrical components and circuit boards in the lens device by virture of the teaching of Kohno.

Onuma and Kohno do not teach that the lens circuit board is connected with the camera board through a **serial** communication line. However, such an interconnect between printed circuit boards using serial bus or serial communication line is well known as taught by Walsh, col. 203, lines 10+.

Therefore, it would have been obvious to one of ordinary skill in the art to connect the lens circuit board to the camera interface board through a serial communication line to reduce the number of interconnect leads between the boards.

Regarding claim 26, Onuma also discloses that the lens control signal is iris control (aperture stop).

Regarding claim 27, it is clear in Onuma that the lens control signal is one of a digital signal and an analog signal (see Abstract).

Regarding claim 28, Onuma also discloses a camera code switch (8, 3) for switching an interface in the camera interface board in accordance with attached camera kind (see Abstract,

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wherein processing program of the arithmetic processing circuit 3 indicates digital environment that must contain digital codes).

Regarding claim 24, also disclosed is a camera connected to the lens device (see Onuma Figs. 1 & 2 and Abstract).

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nhan T. Tran whose telephone number is (703) 605-4246. The examiner can normally be reached on Monday - Thursday, 8:00am - 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew B Christensen can be reached on (703) 308-9644. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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